

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,032,074 B2
APPLICATION NO. : 10/814154
DATED : April 18, 2006
INVENTOR(S) : Blaine Douglas Gaither

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

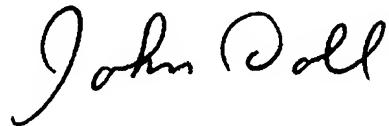
On the title page, in item (73), in "Assignee", in column 1, line 2, after "Company" insert -- , --.

In column 10, lines 61-62, in Claim 5, after "caches," delete "the intermediary caches implemented as coherency filters,".

In column 11, line 2, in Claim 5, after "bus" delete ",".

Signed and Sealed this

Nineteenth Day of May, 2009



JOHN DOLL
Acting Director of the United States Patent and Trademark Office